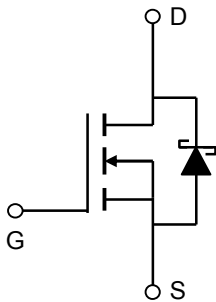
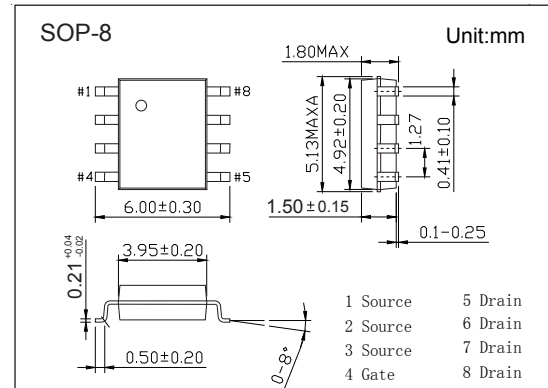


N-Channel MOSFET

AO4714 (KO4714)

■ Features

- $V_{DS} = 30V$
- $I_D = 20 A$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 4.7m\Omega$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 6.7m\Omega$ ($V_{GS} = 4.5V$)
- SRFET™ Soft Recovery MOSFET: Integrated Schottky Diode



■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	V_{DS}	30	V	
Gate-Source Voltage	V_{GS}	± 20		
Continuous Drain Current	I_D	$T_A=25^\circ C$	20	A
		$T_A=70^\circ C$	16	
Pulsed Drain Current	I_{DM}	100		
Avalanche Current	I_{AR}	30		
Repetitive Avalanche Energy	$L=0.3mH$	E_{AR}	135	mJ
Power Dissipation	P_D	$T_A=25^\circ C$	3	W
		$T_A=70^\circ C$	2	
Thermal Resistance.Junction- to-Ambient	R_{thJA}	$t \leq 10s$	40	$^\circ C/W$
		Steady-State	75	
Thermal Resistance.Junction- to-Lead	R_{thJL}	24		
Junction Temperature	T_J	150	$^\circ C$	
Storage Temperature Range	T_{stg}	-55 to 150		

N-Channel MOSFET

AO4714 (KO4714)

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit	
Drain-Source Breakdown Voltage	V _{DSS}	I _D =1mA, V _{GS} =0V	30			V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V			0.1	mA	
		V _{DS} =30V, V _{GS} =0V, T _J =125°C			20		
Gate-Body Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250 μ A	1.2		2.2	V	
Static Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =20A			4.7	mΩ	
		V _{GS} =10V, I _D =20A, T _J =125°C			7.3		
		V _{GS} =4.5V, I _D =16A			6.7		
On State Drain Current	I _{D(ON)}	V _{GS} =10V, V _{DS} =5V	100			A	
Forward Transconductance	g _{FS}	V _{DS} =5V, I _D =20A		90		S	
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =15V, f=1MHz		3760	4512	pF	
Output Capacitance	C _{oss}			682			
Reverse Transfer Capacitance	C _{rss}			314			
Gate Resistance	R _g	V _{GS} =0V, V _{DS} =0V, f=1MHz		0.75	1.5	Ω	
Total Gate Charge (10V)	Q _g	V _{GS} =10V, V _{DS} =15V, I _D =20A		62	74	nC	
Total Gate Charge (4.5V)				29	35		
Gate Source Charge			Q _{gs}		12		
Gate Drain Charge			Q _{gd}		12		
Turn-On DelayTime	t _{d(on)}	V _{GS} =10V, V _{DS} =15V, R _L =0.75Ω, R _{GEN} =3Ω		9.5		ns	
Turn-On Rise Time	t _r			8.5			
Turn-Off DelayTime	t _{d(off)}			34			
Turn-Off Fall Time	t _f			9			
Body Diode Reverse Recovery Time	t _{rr}	I _F =20A, di/dt=300A/us		18	27	nC	
Body Diode Reverse Recovery Charge	Q _{rr}			22			
Maximum Body-Diode Continuous Current	I _S				6	A	
Diode Forward Voltage	V _{SD}	I _S =1A, V _{GS} =0V			0.5	V	

Note. The static characteristics in Figures 1 to 6 are obtained using <300 us pulses, duty cycle 0.5% max.

■ Marking

Marking	4714 KC****
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N-Channel MOSFET AO4714 (KO4714)

Typical Characteristics

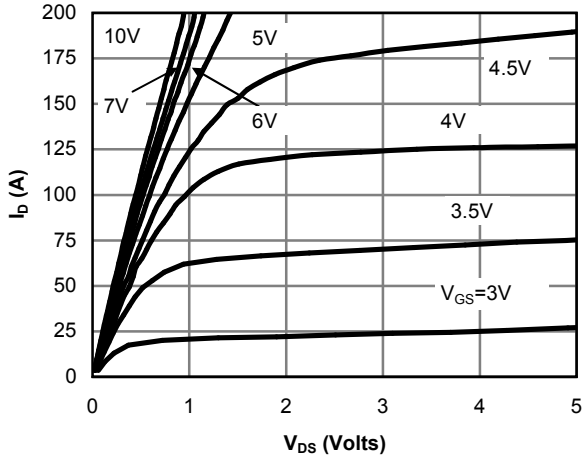


Figure 1: On-Region Characteristics

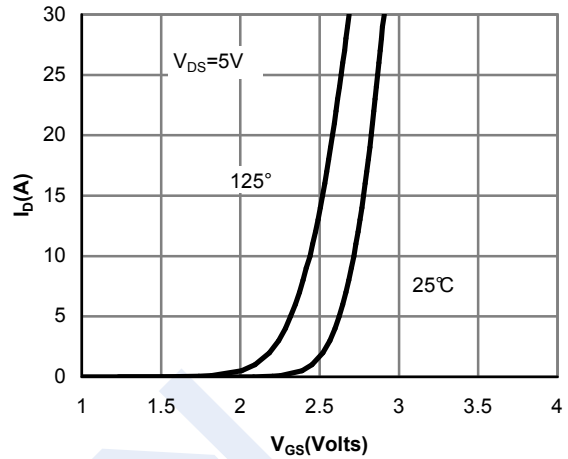


Figure 2: Transfer Characteristics

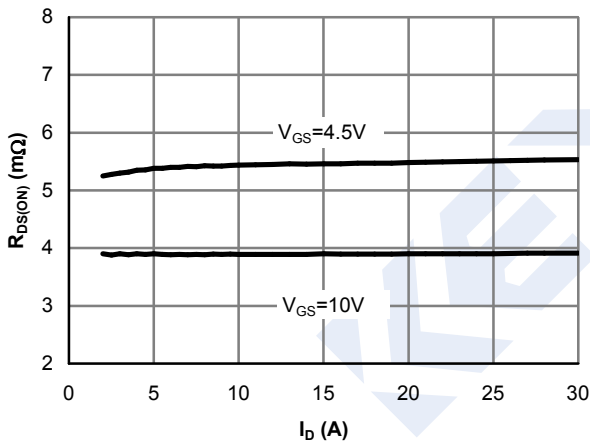


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

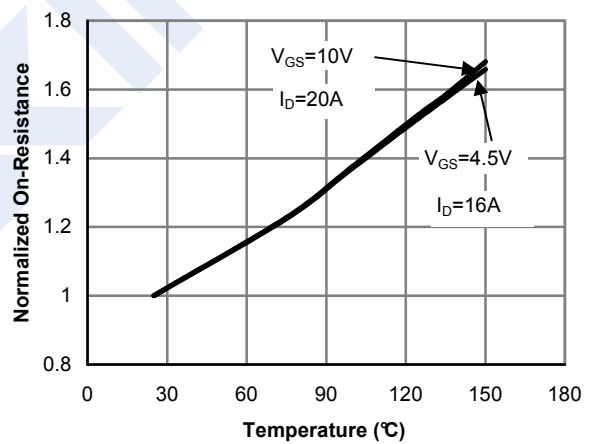


Figure 4: On-Resistance vs. Junction Temperature

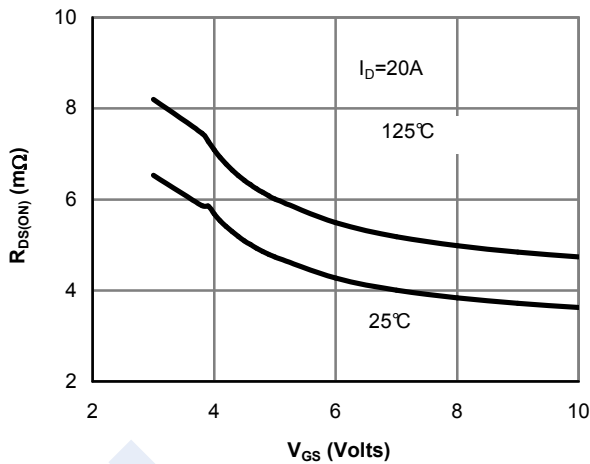


Figure 5: On-Resistance vs. Gate-Source Voltage

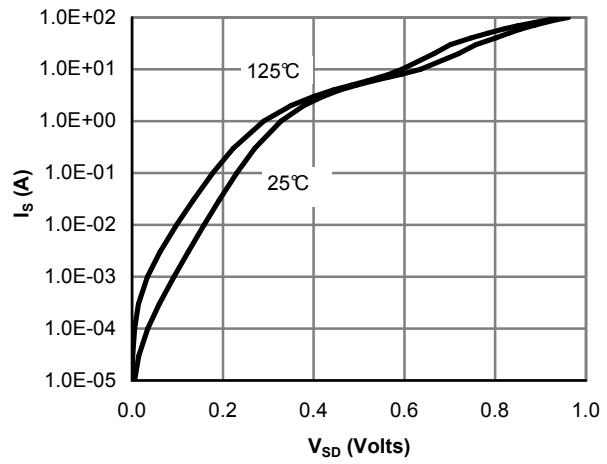


Figure 6: Body-Diode Characteristics

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■ Typical Characteristics

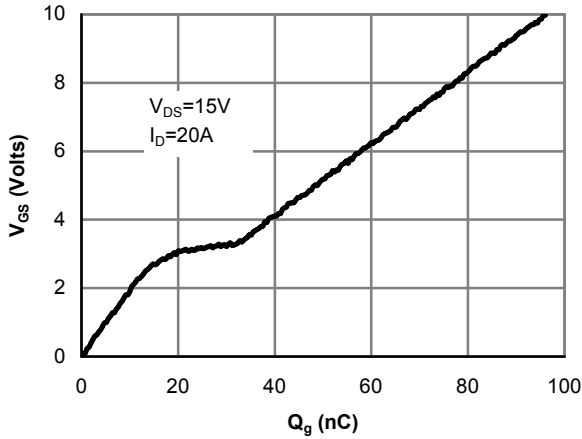


Figure 7: Gate-Charge Characteristics

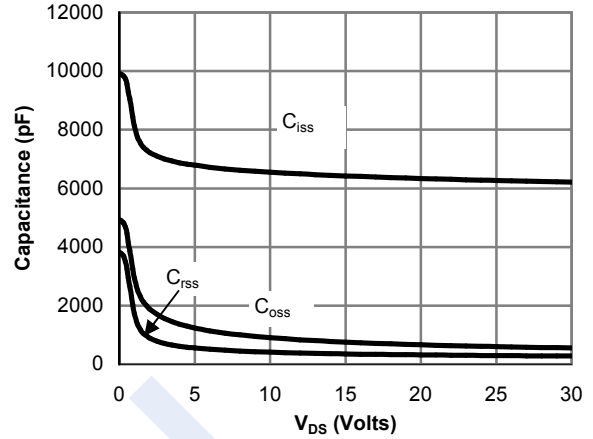


Figure 8: Capacitance Characteristics

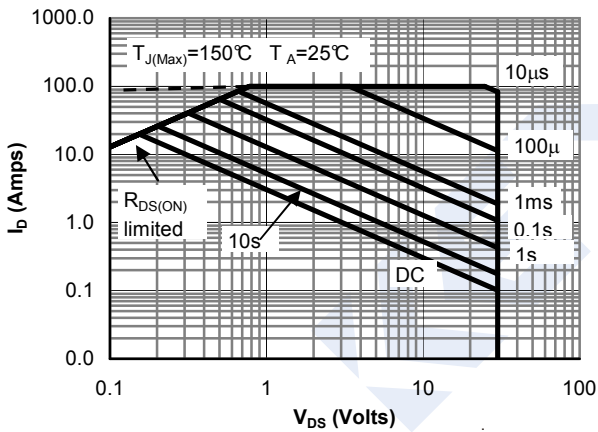


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

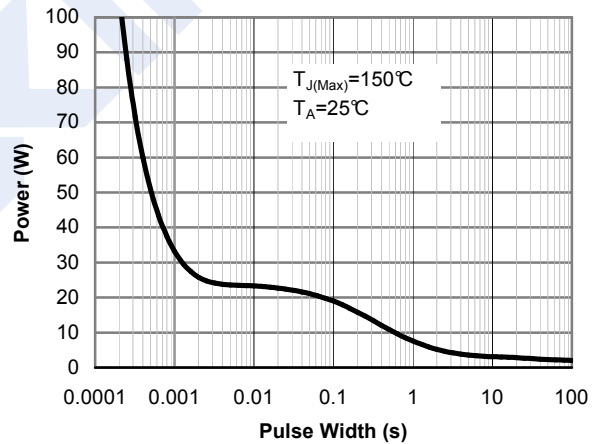


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

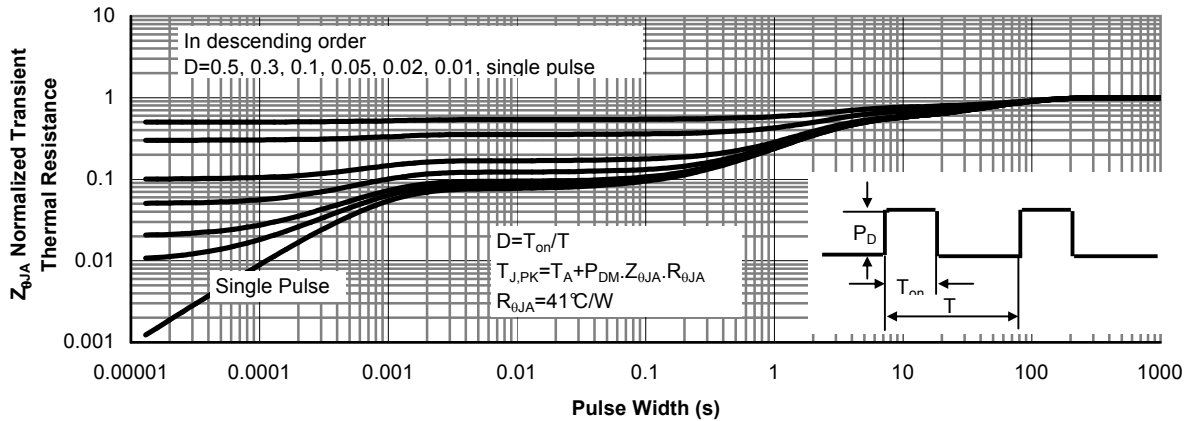


Figure 11: Normalized Maximum Transient Thermal Impedance (Note E)